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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of)
GILBERT P. HYATT)
Serial No. 06/848,017)
Docket No. 307)
Filed: April 3, 1986)
For: AN INTEGRATED CIRCUIT FILTER)
PROCESSOR)
)

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INFORMATION DISCLOSURE STATEMENT

Hon. Assistant Commissioner
For Patents
Washington, D.C. 20231

Transmitted herewith is an Information Disclosure Citation (IDC) listing references for consideration by the Examiner.

The Applicant requests consideration by the Examiner of the references listed on the IDC transmitted herewith and initialing of the references listed on the IDC.

Copies of the listed references are being filed in the PTO contemporaneously herewith.

The listed references are relevant because they were cited by examiners in copending applications of the Applicant.

The instant Information Disclosure Statement is filed in accordance with directions provided by the PTO.¹

1. See the Telephone Conference Record dated February 2, 1999 regarding telephone conversations between SPE Hjerpe and Mr. Turner that took place on December 10, 1998 and February 2, 1999; which Telephone Conference Record was filed with other IDSs of record.

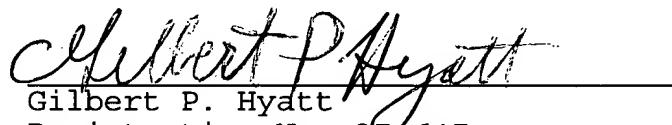
I hereby certify that each of the listed references was cited in one of the actions in the copending applications listed hereinafter, which actions were received no more than three months prior to the mailing of this statement.

| <u>DKT.</u> | <u>SERIAL NO.</u> |
|-------------|-------------------|
| 484 | 08/460,064 |
| 557 | 08/469,532 |

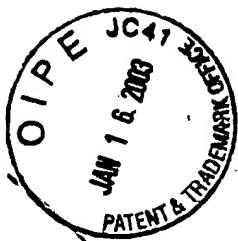
CERTIFICATION OF MAILING BY EXPRESS MAIL: I hereby certify that this correspondence is being deposited with the United States Postal Service with Express Mail post office to addressee service under 37 CFR 1.10, postage prepaid, in an envelope addressed to the Assistant Commissioner for Patents, Washington, D.C. 20231 with the express mail label number EV 069541297 on January 16, 2003.

Respectfully submitted,

Dated: January 16, 2003



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Sheet 1 of 1

Form PTO-1449

**INFORMATION DISCLOSURE CITATION
IN AN APPLICATION**

| | |
|---------------|--------------------|
| Docket Number | Application Number |
| 307 | 06/848,017 |
| Applicant | Gilbert Hyatt |
| Filing Date | Group Art Unit |
| April 3, 1986 | |

U.S. PATENT DOCUMENTS

| EXAMINER INITIAL | DOCUMENT NUMBER | DATE | NAME | CLASS | SUBCLASS |
|------------------|-----------------|---------|--------------|-------|----------|
| | 3,591,784 | 07/1971 | Cutter et al | 708 | 403 |
| | 3,944,728 | 03/1976 | Ferrari | 356 | 75 |

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OTHER DOCUMENTS
(including Author, Title, Date, Pertinent Pages, Etc.)

Beelitz, H.R. et al., System architecture for large scale integration, RCA Laboratories, Princeton NJ, Fall Joint Computer Conference, 1967 pp. 185-200.

"4004 4-bit Central Processor Unit", Intel Chip Description (October 1, 1970) pp. 1 and 2.

NOT
CONSIDERED

| EXAMINER | DATE CONSIDERED |
|---|-----------------|
| EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant. | |